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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tseng

Docket No.:

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Serial No.:

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Art Unit:

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Examiner:

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For:

Compensated Linearity Voltage-Control-Capacitor Device By Standard

CMOS Process

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

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Respectfully submitted,

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